TECHNICAL UNIVERSITY "GHEORGHE ASACHI" IAŞI
FACULTY OF ELECTRONICS, TELECOMMUNICATIONS AND INFORMATION TECHNOLOGY
Bachelor domain: Electronic Engineering, Telecommunications and Information Technologies
Bachelor study program: Telecommunication Technologies and Systems
Academic degree: B.S. (engineer)
Duration of study: 4 years
Degree Program: full time
Language: English

Approved
In Senate meeting from
.....
Senate President,
Prof. Iulian-Aurelian CIOCOIU, PhD.

CURRICULUM

1st Year 2026 - 2027

No.	Discipline name	Disc. code (TTSEN.__)	Opt.	Form.	1st Semester							2nd Semester						
					No. hrs/week/discipli				SI	FE	K	No. hrs/week/discipl				SI	FE	K
					C	S	L	P				C	S	L	P			
101	Mathematical Analysis	MA.101	DOB	DF	2	2	-	-	79	E	**5**							
102	Linear Algebra and Analytical Geometry	LAAG.102	DOB	DF	2	2	-	-	79	E	**5**							
103	Fundamentals of Electrical Engineering 1	FEE1.103	DOB	DF	1	-	2	-	39	V	**3**							
104	Physics 1	PHYS1.104	DOB	DF	1	1	1	-	39	E	**3**							
105	Computer Programming and Programming Languages 1	CPPL1.105	DOB	DF	2	-	2	-	52	E	**4**							
106	Computer-Aided Graphics	CAG.106	DOB	DF	1	-	2	-	39	V	**3**							
107	Foreign Language 1	ENG1.107	DOB	DC	1	1	-	-	26	V	**2**							
108	Sports 1	SP1.108	DOB	DC	-	-	1	-	13	A/R	**1**							
109	Special Mathematics 1	SM1.109	DOB	DF								2	2	-	-	52	E	**4**
110	Fundamentals of Electrical Engineering 2	FEE2.110	DOB	DF								2	-	1	-	66	E	**4**
111	Physics 2	PHYS2.111	DOB	DF								1	1	1	-	66	E	**4**
112	Computer Programming and Programming Languages 2	CPPL2.112	DOB	DF								2	-	2	-	52	V	**4**
113	Passive Components and Circuits	PCC.113	DOB	DF								2	-	2	-	52	E	**4**
114	Foreign Language 2	ENG2.114	DOB	DC								1	1	-	-	26	V	**2**
115	Sports 2	SP2.115	DOB	DC								-	-	1	-	13	A/R	**1**
116	1. Applied Informatics 1	AI1.116.1	DOP	DF	2	-	1	-	66	V	**4**							
	2. Numerical methods	NM.116.2		DF		1	-	-										
117	1. Applied Informatics 2	AI2.117.1	DOP	DF								1	-	2	-	66	V	**4**
	2. Probabilities and Statistics	PS.117.2		DF								2	1	-	-			
118	1. Fundamentals of Sustainable Electronic Technologies	FSET.118.1	DOP	DF								1		2		39	V	**3**
	2. Electronic Board Assembly Technologies. Sustainable Materials	EBAT.118.2		DF								1		2				
119	Education Psychology	EP.119	DFA	DC	2	2	-	-	79	E	**5**							
120	Pedagogy I	PED1.120	DFA	DC								2	2	-	-	79	E	**5**
121	Romanian Language 1	ROM1.121	DFA	DC	-	2	-	-	26	V	**2**							
122	Romanian Language 2	ROM2.122	DFA	DC								-	2	-	-	26	V	**2**
123	Complements of Special Mathematics 1	CSM1.123	DFA	DF								1	1			26	V	**2**
124	Complements of Linear Algebra and Analytical Geometry	CLAAG.124	DFA	DF		1			13	V	**1**							
Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (DOB) and elective discipline (DOP)					**12**	**6** / **7**	**9** / **8**	**0**	**432**	4E 4V 1A/R	**30**	**12** / **13**	**4** / **5**	**11** / **9**	**0**	**432**	4E 4V 1A/R	**30**
					27							**27**						

Legend:

Discipline code: IETTI or SP acronym.discipline acronym.year of study (1-4) serial number (ex: IETTI.AM.101, TTSEN.MA.101)
DOB – Mandatory discipline; DOP – Elective discipline; DFA – Facultative discipline; DF – Fundamental discipline; DS – Specialization discipline; DC – Complementary discipline; C – Course; S – Seminar; L– Laboratory; P – Project; SI – Individual study hours; K – no. of credits; Opt. – optionality category: DOB/ DOP/ DFA; Form. – formative category: DF/ DS/ DC; SI – no. of individual study hours per semester; FE – form of evaluation (E - Exam, V- Verification)

RECTOR
Prof. Dan Caşcaval, PhD.

DEAN
Prof. Daniela Tărniceriu, PhD.

HEAD OF DEPARTMENT
Assoc. Prof. Dănuț Burdia, PhD.

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Senate President,
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CURRICULUM

2nd Year 2026 - 2027

	No.	Discipline name	Disc. Code TTSEN.__	Opt.	Form	1st Semester							2nd Semester						
						No. hrs/week/discipline					FE	K	No. hrs/week/discipline					FE	K
						C	S	L	P	SI			C	S	L	P	SI		
MD	201	Electronic Devices	ED.201	DOB	DF	4	1	2	-	91	E	**7**							
	202	Signals, Circuits and Systems	SCS.202	DOB	DF	4	-	3	-	91	E	**7**							
	203	Computer-Aided Analysis of Electronic Circuits	CAAEC.203	DOB	DF	2	-	2	-	106	E	**6**							
	204	Special Mathematics 2	SM2.204	DOB	DF	3	2	-	-	92	E	**6**							
	205	Object-Oriented Programming	OOP.205	DOB	DF	2	-	2	-	52	V	**4**							
	206	Fundamental Electronic Circuits	FEC.206	DOB	DF								3	-	2	1	105	E	**7**
	207	Signals and Systems	SS.207	DOB	DF								3	-	3	-	78	E	**6**
	208	Digital Integrated Circuits	DIC.208	DOB	DF								3	-	2	1	78	E	**6**
	209	Information Theory	IT.209	DOB	DF								3	-	2	-	92	E	**6**
	210	Electronic and Telecommunications Measurements	ETM.210	DOB	DF								2	-	1	-	39	V	**3**
	211	Sports 3	SP3.211	DOB	DC								-	-	1	-	40	A/R	**2**
ED	213	WEB Technologies	WEBT.213	DFA	DS	2	-	2	-	52	E	4							
	214	Programmable Smart Circuits	PSC.214	DFA	DS	2	-	2	-	25	V	3							
	215	Introduction to Logic Circuits	ILC.215	DFA	DS	1	-	1	-	80	V	4							
	216	Foreign Language 3	FL3.216	DFA	DC	-	1	-	-	13	V	1							
	217	Foreign Language 4	FL4.217	DFA	DC								-	1	-	-	13	V	1
	218	Ethics and Integrity	EI.218	DFA	DC								2	1	-	-	66	V	4
	219	Introduction to Python Programming	IPP.219	DFA	DS								2	-	2	-	25	V	3
	220	Romanian Language 3	ROM3.220	DFA	DC	2	-	2	-	52	E	4							
	221	Romanian Language 4	ROM4.221	DFA	DC								-	2	-	-	26	V	2
	222	Pedagogy II	PED2.222	DFA	DC	2	2	-	-	79	E	5							
	223	Didactics of specialization	DS.223	DFA	DC								2	2	-	-	79	E	5
	224	Intercultural communication	ICC.224	DFA	DC								1	2			39	V	3
		Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)				**15**	**3**	**9**	**0**	**432**	4E 1V	**30**	**14**	**0**	**11**	**2**	**432**	4E 1V 1A/R	**30**
						27							**27**						

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Senate President,
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CURRICULUM

3rd Year 2026 - 2027

	No.	Name of the discipline	Disc.Code ETTI__	Conditionalities	1st Semester							2nd Semester						
					No. hrs/week./discipline					Fin Eval	K	No. hrs/week./discipline					Fin Eval	K
					C	S	L	P	IW			C	S	L	P	IW		
ND	301	Analog Integrated Circuits	EDID301		3	-	2	1	105	E	7							
	302	Introduction to Communications	EDIS302		3	-	1	-	79	C	5							
	303	Television	EDID308		3	-	1	-	52	C	4							
	304	Microcontrollers	EDIS304T		2	-	2	-	52	E	4							
	305	Digital Signal Processing	EDID305T		3	-	2	-	92	E	6							
	306	Decision and Estimation in Data Processing	EDID306T		2	-	2	-	52	E	4							
	307	Computer Networks and Operating Systems	EDIS307T									3	-	2	-	38	C	4
	308	Electronic Equipments for Measuring and Control	EDIS303									3	-	1	1	38	E	4
	309	Communication Systems	EDID309T									3	-	2	-	38	E	4
	310	Computer - Aided Design of Analog and Digital Systems	EDIS310T									2	-	2	1	38	E	4
	311	Power Electronics	EDID311T									2	-	1	-	39	C	3
ED	312	1. Antennas and Propagation	EDOS312T									2	-	1	-	39	E	3
		2. Fundamentals of Radiocommunications	EDOS313T															
IA	313	Domain Training	EDID319									3w x 30h/w=90 h				18	A/R	4
	314	Speciality Training	EDIS320									3w x 30h/w=90 h				18	A/R	4
FD	315	Microcontroller Applications	EDLS314		-	-	-	2	26	C	2							
	316	Sports 4	EDLC315									-	-	2	-	26	A/R	2
	317	Technological Fundamentals of Microelectronics	EDIS309M									2	-	2	-	79	E	5
	318	Analog Circuits Synthesis	EDIS310M									2	-	2	-	52	E	4
	319	Medical Electronics and Informatics	EDOS315E									3	-	2	-	65	E	5
	320	Occupational health and safety specialist	EDLD316									2	2	-	2	24	C	4
	321	Entrepreneurial Education	EDLC317									2	-	1	-	93	C	5
	322	Computer-aided education	EDLC318		1	1	-	-	26	C	2							
	323	Specialized pedagogical practice in pre-university education (1)	EDLC319		14w x 3h/w=42				81	C	3							
	324	Management of pupil class	EDLC320									1	1	-	-	53	E	3
	325	Specialized pedagogical practice in pre-university education (2)	EDLC321									12w x 3h/w=36				53	E	3
	326	Graduation exam, Level I	EDLC322													135	E	5
		Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED)			16	0	10	1	432	4E 2C	30	15	0	9	2	266	4E 2C 2A/R	30
					27							26						

MD – mandatory discipline; **ED** –elective discipline; **FD** –facultative discipline ; **IA** – independent activities; **C** – complementary; **D**- from the domain, **F** – fundamental, **S** – specialization; **V**- verification; **K**- credits; **E**- Exam; **C**- colloquium; **A/R** – accept/reject, **IW** - individual work; *_= credits and verifications not included in the total.T - Telecommunication Technologies and Systems, M – Microelectronics, Optoelectronics and Nanotechnologies, E – Applied Electronics.
Discipline code: ETTI + 3 letters + 3 figures +α, where α = missing / letter = common for the faculty/section .

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CURRICULUM

4th Year 2026 - 2027

	No.	Name of the discipline	Disc. Code ETTI___	Con-ditio-nali-ties	1st Semester							2nd Semester						
					No. hrs/week./discipline					Fin Eval	K	No. hrs/week./discipline					Fin Eval	K
					C	S	L	P	IW			C	S	L	P	IW		
MD	401	Economics and Marketing	EDIC304		2	-	1	-	39	C	3							
	402	Digital Communications	EDIS402T		3	1	1	-	92	E	6							
	403	Electronic Technology	EDIS403T		2	-	-	2	79	C	5							
	404	Radiocommunications	EDIS404T		3	-	1	1	92	E	6							
	405	Microwaves	EDID405T		3	-	2	-	65	C	5							
	406	Mobile Communications	EDIS406									3	-	1	1	38	E	4
	407	Microwave Devices and Circuits for Radiocommunications	EDID407									2	-	1	1	25	C	3
	408	Communication Skills	EDIC408									1	-	-	-	13	VP	1
	409	Management	EDIC412									2	-	1	-	39	C	3
	410	Elaboration of Bachelor Diploma Project	EDIS417									-	-	-	4	52	A/R	4
ED	411	1. Optical Communications	EDOS409T		2	-	2	-	79	E	5							
		2. Introduction to Nanoelectronics	EDOS410T															
	412	1. Noise in Integrated Structures	EDOS411T									2	-	1	1	52	C	4
		2. Electromagnetic Compatibility	EDOS412T															
	413	1. Statistical Data Processing	EDOS413T									3	-	2	-	38	E	4
		2. Advanced Signal Processing Methods	EDOS414T															
	414	1. Telephone Systems	EDOS415T									2	-	2	-	52	E	4
		2. Internet-of-Things	EDOS416T															
IA	415	Elaboration of Bachelor Diploma Project - Practice	EDIS418									2w x 30h/w=60 h				21	A/R	3
	416	Defense of Bachelor Diploma Project	EDIS419									-	-	-	-	270	E	10
FD	417	Analog VLSI Design Techniques	EDIS404M		4	-	-	2	78	E	6							
	418	Digital VLSI Design Techniques	EDIS406M									3	-	-	2	55	E	5
	419	Fundamentals of Data Acquisition Systems	EDID403E									3	-	2	-	55	E	5
	420	Integrated Structures for Specific Applications 2	EDLS425		2	-	2	-	79	C	5							
	421	Functional Verification of Digital Integrated Circuits	EDIA602 TMP		2	-	2	-	79	E	5							
	422	Advanced PCB Design	EDLS422		2	-	2	-	106	E	6							
	423	Automotive Electronics	EDIS408E		3	-	2	-	65	E	5							
	424	Peripheral Equipments and Human-Machine Interface	EDIS410E		2	-	2	-	79	E	5							
	425	Digital Audio Video Systems	EDID406E									2	-	2	-	52	VP	4
	426	Entrepreneurial Education – Digital Business 1	EDLC426		1	-	2	-	93	C	5							
	427	Entrepreneurial Education – Digital Business 2	EDLC427									2	-	-	1	39	C	3
Total hours per week, total assessments per semester and total credits per semester for mandatory discipline (MD) and elective discipline (ED					15	1	8	2	446	3E 3C	30	15	0	8	7	600	3E 3C 1VP 1A/R	30
					26							30						

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